LIST OF CURRENT CLAIMS

1. (Currently Amended) A memory card having a secure digital (SD) interface and a memory stick (MS) interface for communicating with an SD card reader and an MS card reader respectively ~~plurality of different interfaces~~, comprising:

a function module;

a controller coupled to the function module for accessing;

~~a first~~ an SD I/O buffer coupled to the controller for sending a ~~first~~ CMD control signal, the ~~first~~ SD I/O buffer including a first pull-down resistor for providing a first voltage level to the CMD control signal; and

~~a second~~ an MS I/O buffer coupled to the controller for sending a ~~second~~ BS control signal, the ~~second~~ MS I/O buffer including a first pull-up resistor for providing a second voltage level to the BS control signal,

~~wherein the memory card uses the pull-down and the pull-up resistors to separate two of the interfaces by activating one of the interfaces for detecting the first voltage level of the first buffer when the memory card is inserted into a first card reader, such that the first buffer is activated to send the first control signal if the detection of the first voltage level of the first buffer is positive; or by activating another interface for detecting the second voltage level of the second buffer when the memory card is inserted into a second card reader, such that the second buffer is activated to send the second control signal if the detection of the second voltage level of the second buffer is positive~~

wherein, when the memory card is inserted into an SD card reader having a second pull-up resistor, the first pull-down resistor and the second pull-up resistor are coupled to provide the CMD control signal with a high voltage level, so that the controller determines that the memory card is inserted into an SD card reader and thus enables the SD interface for communicating with the SD card reader, and

wherein, when the memory card is inserted into an MS card reader having a second pull-down resistor, the first pull-up resistor and the second pull-down resistor are coupled to provide the BS control signal with a low voltage level, so that the controller determines

that the memory card is inserted into an MS card reader and thus enables the MS interface for communicating with the MS card reader.

2-7 (Cancelled)

8. (New) The memory card as claimed in claim 1, wherein the function module is formed of flash memory.

9. (New) The memory card as claimed in claim 8, wherein the first pull-down resistor has a resistance of 500kΩ.

10. (New) The memory card as claimed in claim 9, wherein the second pull-up resistor has a resistance of 5kΩ.

11. (New) The memory card as claimed in claim 8, wherein the first pull-up resistor has a resistance in the range of 5kΩ to 50kΩ.

12. (New) The memory card as claimed in claim 11, wherein the second pull-down resistor has a resistance in the range of 5kΩ to 50kΩ.